IFW

Docket No.: 04-0175

PATENT



**UNITED STATES PATENT AND TRADEMARK OFFICE**

| | | |
|---|---|---|
| In re Application of: Christopher L. Hamlin | : | By the Examiner: |
| Serial No.: 10/809,939 | : | Express Mail No.: |
| Filed: March 25, 2004 | : | Group Art Unit: |

Title: BROKEN SYMMETRY FOR OPTIMIZATION OF RESOURCE FABRIC IN A SEA-OF-PLATFORM ARCHITECTURE

MS AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8**

I hereby certify that this correspondence is being deposited with the United States Postal Service on July 14, 2004, in a First Class envelope, with sufficient postage thereon, addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Michele D. Norine

Michele D. Norine DATED: July 14, 2004

Dear Sir:

**INFORMATION DISCLOSURE STATEMENT**

Applicant submits herewith patents, publications or other information of which he is aware, which he believes may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR 1.56.

While this Information Disclosure Statement may be "material" pursuant to 37 CFR 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 CFR 1.97(g) the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists.

The attached form, PTO-1449, provides a listing of patents, non-patent literature, publications, or other information as required by 37 CFR 1.98(a)(1).

A copy of each of these foreign patents, non-patent literature and publications listed on PTO-1449 is supplied herewith.

DATED: July 14, 2004

Respectfully submitted,
Christopher L. Hamlin,
LSI Logic Corporation

By: _____
Peng Zhu
Reg. No. 48,063

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In Place of FORM PTO-1449 (Modified) Serial No.: 10/809,939
Applicant: Christopher L. Hamlin
**LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT** Filing Date: March 25, 2004
Group:
Atty. Docket No.: 04-0175



Reference Designation

**U.S. PATENT DOCUMENTS**

| Examiner Initial | Document Number | Date | Name | Class | Subclass | Filing Date if Appropriate |
|---|---|---|---|---|---|---|
| ____AAA | 4,656,592 | Apr. 7, 1987 | Spannenburg, et al. | 364 | 490 | Oct. 10, 1984 |
| ____ABA | 5,752,070 | May 12, 1998 | Martin, et al. | 395 | 800.33 | Jul. 8, 1994 |
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**FOREIGN PATENT DOCUMENTS**

| Examiner Initial | Document Number | Date | Country | Class | Subclass | Translation Yes No |
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| ____AKA | 02202886 JP | Oct. 22, 2001 | Japan | G06F0094 4 | | No |
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**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

Examiner Initial

____ APA "Self-Reconfigurable Programmable Logic Device" by Reetinder Sidhu,et al., File #3115; University of Southern California,Office of Technology Licensing, Los Angeles, CA., www.usc.edu/academe/otl; September 7, 2001

____ AQA "Sea-of-IP: An Ocean of Design Possibilities", Royal Philips Electronics; Philips Semiconductor-Technology Home Pages; www.semiconductor.phiips.com/technology/sea-of-ip/index.html; 2/22/02; 3 pages

____ ARA "Tensilica Navigates 'Sea of Processors' Designs" by Chris Edwards; Electronics Times; June 14, 2001; www.eetimes.com, News Channels Semiconductors; 3 pages

Examiner: Date Considered:

EXAMINER: Initial if reference considered, whether nor not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

In Place of FORM PTO-1449 (Modified

**LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT**

Serial No.: 10/809,939
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|---|---|---|---|---|---|---|
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| ABA | | | | | | |
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**FOREIGN PATENT DOCUMENTS**

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_____ APA "On-Chip Networks Weighed a Wiring Alternative" by Ron Wilson, Integrated System Design, June 25, 2001; www.eetimes.com; News Channels Design Automation; www.eedesign.com/article/printableArticle.jhtml?articleID=12805718; 6/30/2004/; 2 pages

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_____ ARA Intel® XScale™Microarchitecture: Product Brief; Copyright 2000 Intel Corporation; www.intel.com/design/intelxscale/bench.htm; 2 pages; 6/30/2004

Examiner: Date Considered:

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